G.703 Fractional T1 Access Unit

User Guide

Description

The **G.703 T1/FT1 ACCESS UNIT** is a single port access unit for T1 or Fractional T1 services. Data Port rates are selectable via DIP-switches, for any multiple of 64 Kbps up to 1536 Kbps. User data is placed into the T1 frame, using only the required number of timeslots. Timeslot assignment is accomplished according to the Data Port speed and is also selected by DIP switches. The main T1 link may be clocked from the recovered receive clock (LBT). The data channel interface is RS-530 standard. Adapter cables are available for V.35, X.21, and RS-449. The **G.703 T1/FT1**'s DIP and slide switches, located on the front and side panels, provide for easy setup and control of functions.

Features

- LTU (Line Terminating Unit) built in unit.
- Single port access to T1 & Fractional T1 services.
- Interface conversion between G.703 and RS-530, RS-449(V.36), X.21, or V.35.
- Data rate: DIP selectable Sync Nx64Kbps 1536Kbps.
- Fully transparent signal conversion under unframed mode (1544Kbps).
- Clock Regeneration from incoming AMI or B8ZS Data.
- Diagnostics Loopbacks both for G.703 (Recovery) and Data port side.
- ♦ All 1's monitor.
- Decoded data in NRZ form.
- DC +9V input power.

SPECIFICATIONS

CCITT G.703 interface specifications :

٠	TYPE	Bidirectional (T1) 1544Kbps.
\blacklozenge	Line	4 wires, 16-26AWG.
\blacklozenge	Range	Up to 655feet/ AWG#24 better.
\blacklozenge	Impedance	100ohm.
\blacklozenge	"Pulse" amplitude	Nominal 3.00V.
\blacklozenge	"Zero" amplitude	+/- 0.1V max.
\blacklozenge	Clock frequency	1.544 MHz.
\blacklozenge	Frequency tracking	+/- 50ppm
٠	Interface connector	RJ-45.
\blacklozenge	Complies with	ITU-T G.703; G.704; G.706;
		and G.824.
٠	Frame format	CRC6 ON/OFF, DF/ESF,
		Unframed/Framed.
\blacklozenge	Line code	AMI or B8ZS.

Data communications interface specifications :

•	Interface T	ype
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- Data rate
- Connector
- Line code

RS-530/DB25 standard or V.35, RS-449(V.36), X.21 with adapter cables. 64Kbps - 1.544Mbps. DB25/F with adapter cables. NRZ.

INDICATORS

◆ DTE	[Green LED]
◆ DCE	[Green LED]
♦ TD	[Yellow LED]
♦ RD	[Yellow LED]
◆ TEST	[Red LED]
♦ Alarm	[Red LED]

T-Clk loss [Red LED]
R-Sig loss [Red LED]

Data port set DTE mode. Data port set DCE mode. Transmit data indication. Receive data indication. Loopback test indication. Indication for Data error; Sync loss; Frame loss; Remote alarm. Transmit clock loss. Receive signal loss.

OPTIONAL CABLES

Ordering Part No.	Female	Male	
G703/V35	V.35F	V.35M	
G703/449	RS449F	RS449M	
G703/X21	X.21F	X.21M	
G703/530	RS530F	RS530M	

OPERATION: SLIDE SWITCH FUNCTION

There are two slide switches on the unit. The **POWER** switch is a two position slide switch that controls the flow of DC power to the internal circuitry.

NOTE: Changes to the DIP switch settings are only recognized during CPU initialization. Use the **POWER** switch to cycle the DC off and then on, this will RESET the CPU and it will "read" the new DIP switch settings.

The other slide switch on the **G.703 Pack** is for loopback operations. The middle position is for normal operation. The loopback positions provide an internal loopback on either the Data Port or the Recovery Port.

OPERATION: DIP SWITCH FUNCTIONS

Refer to the following tables for the functions of the DIP switches located along the side of the **G.703 Pack**. The **G.703 T1/FT1** provides a total of 24 DIP switches for timeslot setting. Each switch will enable or disable one 64Kbps timeslot (follow the Nx64 formula).

For example, if you wish to translate only 128K from a 1544K T1 line, you only need to set two dip switches (128K÷64K=2). You may select any two of the 24 timeslots.

SW. No.	DIP STATUS		US	FUNCTIONS	
SW1	1	2	3		
	OFF	OFF	OFF	DSX-1 0-133 feet / CSU 0 dB	
	OFF	OFF	ON	DSX-1 133 to 266 feet	
	OFF	ON	OFF	DSX-1 266 to 399 feet	
	OFF	ON	ON	DSX-1 399 to 533 feet	
	ON	OFF	OFF	DSX-1 533 to 655 feet	
	ON	OFF	ON	CSU -7.5 dB	
	ON	ON	OFF	CSU -15 dB	
	ON	ON	ON	CSU -22.5 dB	
	4	OFF		Receive Equalizer Gain Limit -36dB	
	4	ON		Receive Equalizer Gain Limit -30dB	
	5	OFF		Line Code B8ZS	
	5	ON		Line Code AMI	
SW2		1 thru 8		Timeslot 1 thru 8	
SW3		1 thru 8		Timeslot 9 thru 16	
SW4		1 thru 8		Timeslot 17 thru 24	
SW5	1 7 8		8	Test Mode:	
	OFF	OFF	OFF	Normal Mode	
	OFF	OFF	ON	Diagnostics Test Mode	
SW6	1	2		Rx/Tx timing source	
	OFF	OFF		$Rx \leftarrow Recovery, Tx \leftarrow Data port$	
	OFF	ON		Rx/Tx← Data port	
	ON	OFF		$Rx/Tx \leftarrow Recovery$	
	ON	ON		Rx/Tx ← Internal Oscillator	
	2	OFF		Rx Clk Polarity - Normal	
	3	ON		RX Clk Polarity - Inverted	
	4	OFF		Tx Clk Polarity - Normal	
	4	ON		Tx Clk Polarity - Inverted	
	5	OFF		CRC6 Disabled	
	5	ON		CRC6 Enabled	
	6 OFF ON 7 OFF			ESF frame mode	
				D4 frame mode	
				Idle code is MARK (FFh)	
	/	ON		Idle code is FLAG (7Eh)	
	0	OFF		Data port is DCE	
	ð	ON		Data port is DTE	

Table 1, G.703 T1/FT1 Pack DIP Switch Quick Reference.

G.703 T1 DB25	G.703 T1 DB25 PIN DEFINITIONS						
DB25 FEMALE	PIN	DTE	DCE	COMMENT			
PIN NUMBER	NAME	MODE	MODE				
1	SHIELD						
2	TD(A)	OUTPUT	INPUT				
3	RD(A)	INPUT	OUTPUT				
4	RTS(A)			NOTE 1			
5	CTS(A)			NOTE 1			
6	DSR(A)			NOTE 2			
7	GROUND						
9	RC(B)	INPUT	OUTPUT				
11	ETC(B)	OUTPUT	INPUT				
12	TC(B)	INPUT	OUTPUT				
13	CTS(B)			NOTE 3			
14	TD(B)	OUTPUT	INPUT				
15	TC(A)	INPUT	OUTPUT				
16	RD(B)	INPUT	OUTPUT				
17	RC(A)	INPUT	OUTPUT				
18	POWER IN						
19	RTS(B)			NOTE 3			
20	DTR(A)			NOTE 2			
22	DSR(B)			NOTE 4			
23	DTR(B)			NOTE 4			
24	ETC(A)	OUTPUT	INPUT				
25	GROUND			NOTE 5			

Table 2, Pin Definitions of G.703 Pack Data Port

NOTE 1: RTS(A) AND CTS(A) INTERNALLY CONNECTED TOGETHER. NOTE 2: DSR(A) AND DTR(A) INTERNALLY CONNECTED TOGETHER. NOTE 3: RTS(B) AND CTS(B) INTERNALLY CONNECTED TOGETHER. NOTE 4: DSR(B) AND DTR(B) INTERNALLY CONNECTED TOGETHER. NOTE 5: This pin used by *HCT-6000* to indicate presence of **G.703 Pack**.

DIP SW1: SETTING LINE INTERFACE PARAMETER							
D	IPSW N	lo.	SV	V STAT	ГЕ	FUNCTIONS	
1	2	3	OFF	OFF OFF OFF		DSX-1 0 TO 133 feet / CSU 0dB	
			OFF	OFF	ON	DSX-1 133 to 266 feet	
			OFF	ON	OFF	DSX-1 266 to 399 feet	
			OFF	OFF ON ON		DSX-1 399 to 533 feet	
ON OFF		OFF	OFF	DSX-1 533 to 655 feet			
ON OFF		OFF	ON	CSU -7.5 dB			
			ON	ON	OFF	CSU -15 dB	
			ON	ON	ON	CSU - 22.5 dB	
			OFF			Receive Equalizer Gain Limit -36dB	
4		ON			Receive Equalizer Gain Limit -30dB		
5		OFF			Line code is B8ZS		
	3		ON			Line code is AMI	

Table 3, G.703 T1/FT1 Pack Setting Line Interface Parameter.

DIP SW2: SETTING TIME SLOT 1-8					
DIPSW NO.	SW STATE	FUNCTION	COMMENT		
1	OFF	TIME SLOT 1 IDLE			
1	ON	TIME SLOT 1 ACTIVE			
2	OFF	TIME SLOT 2 IDLE			
2	ON	TIME SLOT 2 ACTIVE			
2	OFF	TIME SLOT 3 IDLE			
5	ON	TIME SLOT 3 ACTIVE			
4	OFF	TIME SLOT 4 IDLE			
4	ON	TIME SLOT 4 ACTIVE			
5	OFF	TIME SLOT 5 IDLE			
5	ON	TIME SLOT 5 ACTIVE			
6	OFF	TIME SLOT 6 IDLE			
0	ON	TIME SLOT 6 ACTIVE			
7	OFF	TIME SLOT 7 IDLE			
7	ON	TIME SLOT 7 ACTIVE			
0	OFF	TIME SLOT 8 IDLE			
0	ON	TIME SLOT 8 ACTIVE			

Table 4, G.703 T1/FT1 Pack Setting TimeSlot 1-8.

NOTE: DIPSW2 to DIPSW4 all OFF is UNFRAMED MODE, the Clock Rate is FIXED at 1554K. In FRAMED MODE, the Clock Rate = 64KxN (N = NUMBER OF TIME SLOTS, 1-24).

DIP SW3: SETTING TIME SLOT 9-16					
DIPSW NO.	SW STATE	FUNCTION	COMMENT		
1	OFF	TIME SLOT 9 IDLE			
1	ON	TIME SLOT 9 ACTIVE			
2	OFF	TIME SLOT 10 IDLE			
2	ON	TIME SLOT 10 ACTIVE			
2	OFF	TIME SLOT 11 IDLE			
5	ON	TIME SLOT 11 ACTIVE			
4	OFF	TIME SLOT 12 IDLE			
4	ON	TIME SLOT 12 ACTIVE			
5	OFF	TIME SLOT 13 IDLE			
5	ON	TIME SLOT 13 ACTIVE			
6	OFF	TIME SLOT 14 IDLE			
0	ON	TIME SLOT 14 ACTIVE			
7	OFF	TIME SLOT 15 IDLE			
7	ON	TIME SLOT 15 ACTIVE			
8	OFF	TIME SLOT 16 IDLE			
0	ON	TIME SLOT 16 ACTIVE			

Table 5, G.703 T1/FT1 Pack Setting TimeSlot 9-16.

DIP SW4: SETTING TIME SLOT 17-24						
DIPSW NO.	SW STATE	FUNCTION	COMMENT			
1	OFF	TIME SLOT 17 IDLE				
1	ON	TIME SLOT 17 ACTIVE				
2	OFF	TIME SLOT 18 IDLE				
2	ON	TIME SLOT 18 ACTIVE				
3	OFF	TIME SLOT 19 IDLE				
5	ON	TIME SLOT 19 ACTIVE				
4	OFF	TIME SLOT 20 IDLE				
4	ON	TIME SLOT 20 ACTIVE				
5	OFF	TIME SLOT 21 IDLE				
5	ON	TIME SLOT 21 ACTIVE				
6	OFF	TIME SLOT 22 IDLE				
0	ON	TIME SLOT 22 ACTIVE				
7	OFF	TIME SLOT 23 IDLE				
1	ON	TIME SLOT 23 ACTIVE				
8	OFF	TIME SLOT 24 IDLE				
0	ON	TIME SLOT 24 ACTIVE				

Table 6, G.703 T1/FT1 Pack Setting TimeSlot 17-24.

DIP SW5: SETTING DIAGNOSTIC TEST					
DIPSW NO.	SW STATE	FUNCTION	COMMENT		
1 thru 7	OFF	Not Assigned. Must set to OFF.			
0	OFF	Normal Mode			
0	ON	Diagnostic Test Mode			

Table 7, G.703 T1/FT1 Pack Setting Diagnostics Test.

DIP SW	6: FUN	CTIONS	5		
DIP SW NO.		SW STATE		FUNCTION	COMMENT
1 2 OFF OFF		OFF	RX timing from recovery,	Reference	
				TX timing from data port. (X'parent)	DTE/DCE
		OFF	ON	RX and TX timing all from data port.	Timing Source
		ON	OFF	RX and TX timing all from recovery.	select Table 9.
		ON	ON	RX and TX timing all from internal	
				oscillator.	
	2	OFF		RX clock polarity: NORMAL	
	5	ON		RX clock polarity: INVERT	
4		OFF		TX clock polarity: NORMAL	
-	+	ON		TX clock polarity: INVERT	
4	5	OFF		CRC6 select: DISABLE	
)	ON		CRC6 select: ENABLE	
	5	OFF		Frame mode select: ESF	
0		ON		Frame mode select: D4	
7		OFF		IDLE code select: MARK(0xFF)	
		ON		IDLE code select: FLAG(0x7E)	
	2	0	FF	DATA PORT set to DCE	
8		ON		DATA PORT set to DTE	

Table 8, G.703 T1/FT1 Pack DIP SW6 Functions.

DTE/E	DTE/DCE TIMING SOURCE SELECT TABLE												
DIP	SW6 FING	DTE	MODE	DCE MODE									
1	2	RX TIMING	TX TIMING	RX TIMING	TX TIMING								
OFF	OFF	Recover from T1 port Output to ETC pins.	Source from data port. Input from RC pins.	Recover from T1 port. Output to RC/TC pins.	Source from data port. Input from ETC pins.								
OFF	ON	Source from data port. Input from RC pins. Output to ETC pins.	Source from data port. Input from RC pins.	Source from data port. Input from ETC pins. Output to RC/TC pins.	Source from data port. Input from ETC pins.								
ON	OFF	Recover from T1 port Output to ETC pins.	Source from T1 port.	Recover from T1 port. Output to RC/TC pins.	Source from T1 port.								
ON	ON	From internal oscillator. Output to ETC pins.	From internal oscillator.	From internal oscillator. Output to RC/TC pins.	From internal oscillator.								

Table 9, G.703 T1/FT1 Pack Timing Source.

LED INDICATORS TABLE										
LED NAME	COLOR	INDICATION								
DTE	GREEN	DATA PORT DTE MODE & POWER ON								
DCE	GREEN	DATA PORT DCE MODE & POWER ON								
TD	YELLOW	TRANSMIT DATA STATUS								
		(ON:1/OFF:0/FLASH:CHANGE)								
RD	YELLOW	RECEIVE DATA STATUS								
		(ON:1/OFF:0/FLASH:CHANGE)								
TEST	RED	LOOPBACK TEST (ON:TEST/OFF:NORMAL)								
ALARM	RED	DETAIL SEE NEXT TABLE								
		(ON:TEST/OFF:NORMAL)								
TX CLOCK LOSS	RED	TRANSMIT CLOCK LOSS								
RX SIGNAL LOSS	RED	RECEIVE SIGNAL LOSS								

Table 10, **G.703 T1/FT1 Pack** LED Indicators.

ALARM LED INDICATORS TABLE									
UNFRAMED MODE	FRAMED MODE								
CODE VIOLATION	CODE VIOLATION								
RX DATA ALL ONES	RX DATA ALL ONES								
	YELLOW ALARM								
	RECEIVER LOSS OF SYNC								

Table 11, G.703 T1/FT1 Pack Alarm LED Indicators.

EXAMPLES OF DIP SWITCH SETTINGS

Note: X means "don't care"

Example 1:

Line length 0-133 feet: DIPSW1-1 to DIPSW1-3 OFF.

Receive Equalizer Gain Limit -30dB: DIPSW1-4 ON.

Line code AMI: DIPSW1-5 ON.

UNFRAMED mode, Clock rate fixed 1554Khz: DIPSW2-1 to DIPSW4-8 OFF. Rx timing from T1 recovery, Tx timing from Data Port: DIPSW6-1&2 OFF. Data port receive clock polarity, NORMAL: DIPSW6-3 OFF. Data port transmit clock polarity, NORMAL: DIPSW6-4 OFF. Data port set to DCE mode: DIPSW6-8 OFF.

		D	IPSV	V1									DIPS	SW2			
	1	2	3	4	5					1	2	3	4	5	6	7	8
OFF									OFF								
ON									ON								
						•											
				DIP	SW3								DIPS	SW4			
	1	2	3	4	5	6	7	8		1	2	3	4	5	6	7	8
OFF									OFF								
ON									ON								
				DIP	SW5								DIP	SW6			
	1	2	3	4	5	6	7	8		1	2	3	4	5	6	7	8
OFF									OFF					Х	Х	Х	
ON									ON					X	X	X	

Example 2:

Line length 399-533 feet: DIPSW1-1 OFF, DIPSW1-2&3 ON. Receive Equalizer Gain Limit -36dB: DIPSW1-4 OFF. Line code B8ZS: DIPSW1-5 OFF. FRAMED mode, Clock rate 64Khz, Using timeslot 4: DIPSW2-5 ON, Others DIPSW2 to DIPSW4 OFF. Rx and Tx timing from T1 Rx recovery: DIPSW6-1 ON DIPSW6-2 OFF. Data port receive clock polarity, INVERT: DIPSW6-3 ON. Data port transmit clock polarity, NORMAL: DIPSW6-4 OFF. Data port set to DTE mode: DIPSW6-8 ON.

		D	IPSV	V1	_	1					_		DIP	SW2	_	_	_
	1	2	3	4	5					1	2	3	4	5	6	7	8
OFF									OFF								
ON									ON								
						•											
				DIP	SW3								DIP	SW4			
	1	2	3	4	5	6	7	8		1	2	3	4	5	6	7	8
OFF									OFF								
ON									ON								
									-								
				DIP	SW5								DIP	SW6			
	1	2	3	4	5	6	7	8		1	2	3	4	5	6	7	8
OFF									OFF					Х			
ON									ON					Х			

Example 3:

Line length 133-266 feet: DIPSW1-1&2 OFF, DIPSW1-3 ON. Receive Equalizer Gain Limit -36dB: DIPSW1-4 OFF. Line code B8ZS: DIPSW1-5 OFF. FRAMED mode, Clock rate 1024Khz, Using timeslot 1-4, 9,10, 15-24 Rx and Tx timing from DATA PORT: DIPSW6-1 OFF DIPSW6-2 ON. Data port receive clock polarity, NORMAL: DIPSW6-3 OFF. Data port transmit clock polarity, INVERT: DIPSW6-4 ON. Data port set to DTE mode: DIPSW6-8 ON.

		D	IPSV	V1									DIP	SW2			
	1	2	3	4	5					1	2	3	4	5	6	7	8
OFF									OFF								
ON									ON								
				DIP	SW3				1	İ			DIP	SW4			
	1	2	3	4	5	6	7	8		1	2	3	4	5	6	7	8
OFF									OFF								
ON									ON								
									4								
				DIP	SW5								DIP	SW6			
	1	2	3	4	5	6	7	8		1	2	3	4	5	6	7	8
OFF									OFF								
ON									ON								

Example 4:

Line length 133-266 feet: DIPSW1-1&2 OFF, DIPSW1-3 ON. Receive Equalizer Gain Limit -36dB: DIPSW1-4 OFF. Line code B8ZS: DIPSW1-5 OFF. FRAMED mode, Clock rate 1536Khz, Using timeslot 1-24 Rx and Tx timing from INTERNAL OSC: DIPSW6-1&2 ON. Data port receive clock polarity, INVERT: DIPSW6-3 ON. Data port transmit clock polarity, INVERT: DIPSW6-4 ON. Data port set to DCE mode: DIPSW6-8 OFF.

		D	IPSV	V1									DIP	SW2			
	1	2	3	4	5					1	2	3	4	5	6	7	8
OFF									OFF								
ON									ON								
				DIP	SW3								DIP	SW4			
	1	2	3	4	5	6	7	8		1	2	3	4	5	6	7	8
OFF									OFF								
ON									ON								
		-	-						•								
				DIP	SW5					DIPSW6							
	1	2	3	4	5	6	7	8		1	2	3	4	5	6	7	8
OFF									OFF								
ON									ON								

G.703 T1/FT1 Access Pack factory settings.



Line length: 0-133 feet.

Receive Equalizer Gain Limit: -36dB.

Line code: B8ZS.

UNFRAMED mode.

Clock: 2048KHz

Rx timing from Recovery.

Tx timing from Data Port.

Data port receive clock polarity, NORMAL.

Data port transmit clock polarity, NORMAL.

Data port set to DCE mode.

Interface PIN Assignment

Data Port : RS-530 DB-25 Female

DESIGNATION NO. Clear to Send (B)	Return Return
DTE Ext. Transmit Element Timing (A) 24 C 11 Ext. Transmit Signal Element Timing (B) - Refum DTE Readu (B) 23 C 11 Ext. Transmit Signal Element Timing (g (B) Return
Dic Ready (b) 23 36 10 Return DCE Ready (B) 22 36 -9 Receiver Signal Element Tirning (B) 21 36 -9 Receiver Signal Element Tirning (B)	Return
DTE Ready (A) 20 - 7 Signal Ground	Common
DTE	DCE
Return Received Data (B) 16 3 Received Data (A) DDF	DCE

G.703 Port: RJ-45

- 1 NC
- 2 GND
- 3 Transmit (-)
- 4 Receive (-)
- 5 Receive (+)
- 6 Transmit (+)
- 7 GND
- 8 NC



G.703 RJ-45 cable PINOUT PIN PIN 1 1 4 2 2 3 3 4 4 5 5 6 6 7 7 8 8



G.703 T1



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